

## Electrical Engineering

1. Accelerometer
2. Actuator
3. Adder Design, Ripple Carry  
Adder vs. Carry Look-Ahead  
Adder
4. AM (amplitude modulation)
5. Ampere's law
6. AND gate
7. assembler
8. assembly language
9. Asynchronous detection
10. Balanced three-phase circuit
11. bandwidth
12. bit rate
13. Boolean expression
14. Bus Arbitration, Fixed Priority  
vs. Round-robin priority
15. capacitance
16. Circuit analysis
17. closed-loop control
18. CMOS (Complementary Metal  
Oxide Semiconductor) logic
19. compiler
20. content-addressable memory
21. continuous time system
22. current source
23. determinant of a matrix
24. Diffusion current
25. Diode detector
26. discrete time system
27. DRAM (Dynamic Random  
Access Memory)
28. electromagnetic wave
29. Encoder
30. error probability
31. feedback control system
32. filters (low-pass, band-pass,...)
33. Flash Memory
34. flip-flop
35. FM (frequency modulation)
36. FOR-loop, WHILE-loop,  
recursive function call
37. Fourier series
38. Fourier transforms
39. FPGAs (Field Programmable  
Gate Arrays)
40. frequency response
41. full-adder
42. Ground reference
43. Half-Adder, Full-Adder, Ripple-  
Carry Adder
44. high-level language
45. Impulse of response
46. impulse response
47. inductance
48. Interrupt driven I/O  
(input/output) vs. Program driven  
I/O
49. Karnaugh map
50. Kirchhoff's current law
51. Kirchhoff's voltage law
52. Laplace transform
53. Latches, Flip-Flops, Counters
54. linear circuit
55. Linear equivalent circuit
56. local-area network
57. Logic gate
58. Magnetism
59. matrix
60. Maxwell's equations
61. Mesh current
62. Microprocessor
63. modulation and demodulation
64. Multiplexer
65. NOT function
66. Observability
67. Ohm's law
68. open-loop control
69. OR gate
70. oscilloscope
71. Output-rate control
72. packet
73. Permeability
74. Phasor diagram
75. phasor diagram
76. Potential energy barrier
77. probability density
78. protocol

79. random variable
80. random-access memory
81. recursive filter
82. resistance
83. resonant circuit
84. ROM (Read Only Memory),  
RAM (Random Access Memory)
85. root-locus
86. Rotating field
87. Semiconductor
88. sensor
89. signal generator
90. signal-to-noise ratio
91. Sinusoidal steady-state analysis
92. stability criteria
93. step response
94. subroutine
95. Synchronous systems -- Clock  
and Clock Skew
96. Tachometer generator
97. Timing analysis of a digital  
circuit
98. Transmission gates in VLSI
99. voltage source
100. wide-area network

**Electrical Engineering Terms**